

(21) Application No 9813556.9

(22) Date of Filing 23.06.1998

(30) Priority Data

(31) 09166104

(32) 23.06.1997

(33) JP

(71) Applicant(s)

NEC Corporation

(Incorporated in Japan)

7-1 Shiba 5-chome, Minato-ku, Tokyo 108-01, Japan

(72) Inventor(s)

Tetsuya Takaki

(74) Agent and/or Address for Service

Mathys & Squire

100 Grays Inn Road, LONDON, WC1X 8AL,
United Kingdom

(51) INT CL⁶

H04B 1/707

(52) UK CL (Edition P)

H3G GSX G12P G15

(56) Documents Cited

WO 94/03002 A1

(58) Field of Search

UK CL (Edition P) H3G GPDP GPP GPXX GSX, H3W

WUL WVT WVX

INT CL⁶ H03G, H04B 1/69 1/707, H04J 13/00 13/02
13/04

ONLINE:WPI

(54) Abstract Title

Spread spectrum radio receiver in which the gain is controlled in dependence on data error produced by interfering signals

(57) A radio receiver is provided, which prevents the sensitivity of the receiver from degrading even if the receiver is used in a communication system using a plurality of channels with unequal transmission powers. This radio receiver is comprised of an antenna for receiving a wanted RF signal, a variable-gain RF stage 3, 4 for amplifying the wanted RF signal to output an amplified, gain-controlled RF signal, a frequency converter 6 for frequency-converting the amplified, gain-controlled RF signal to output an IF signal, a variable-gain IF amplifier 9 for amplifying the IF signal to output an amplified, gain-controlled IF signal, a despreader 10 for despreading the amplified, gain-controlled IF signal to output a baseband signal, a Fourier transformer 13 for conducting a Fourier transformation with respect to the baseband signal, a jamming-wave detector 14 for detecting a jamming wave existing in a frequency range of the wanted RF signal, a demodulator 12 for demodulating the baseband signal to output an information signal, an error-rate calculator 15 for calculating an error rate of the information signal, and a controller 16 for controlling gains of the variable-gain RF stage and the variable-gain IF amplifier.

FIG. 3

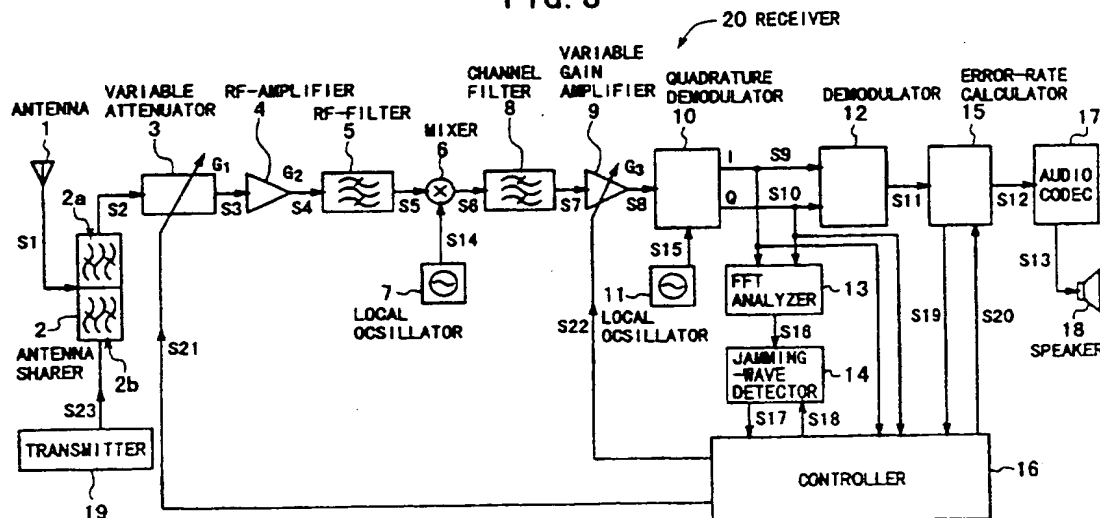


FIG. 1

PRIOR ART

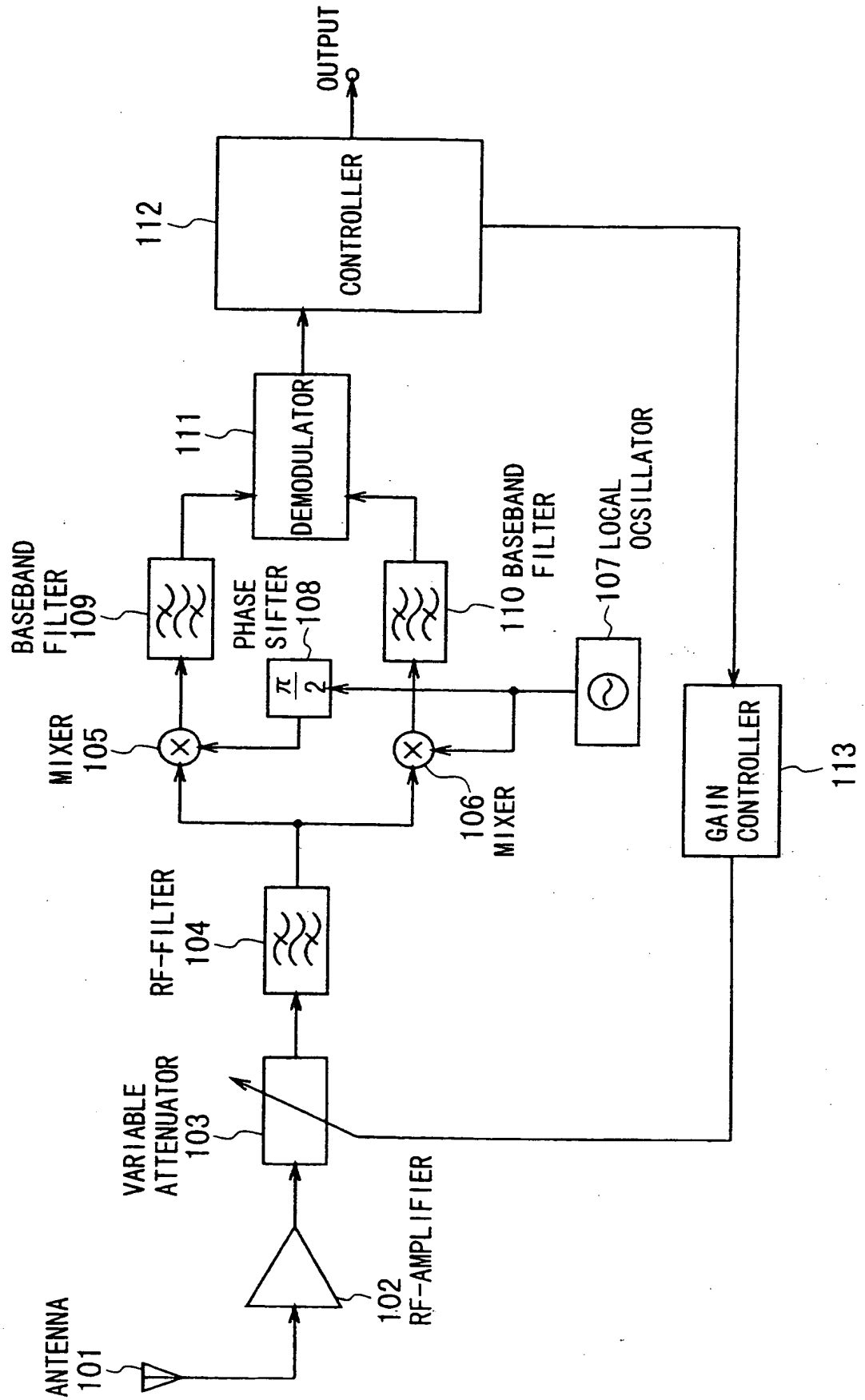


FIG. 2
PRIOR ART

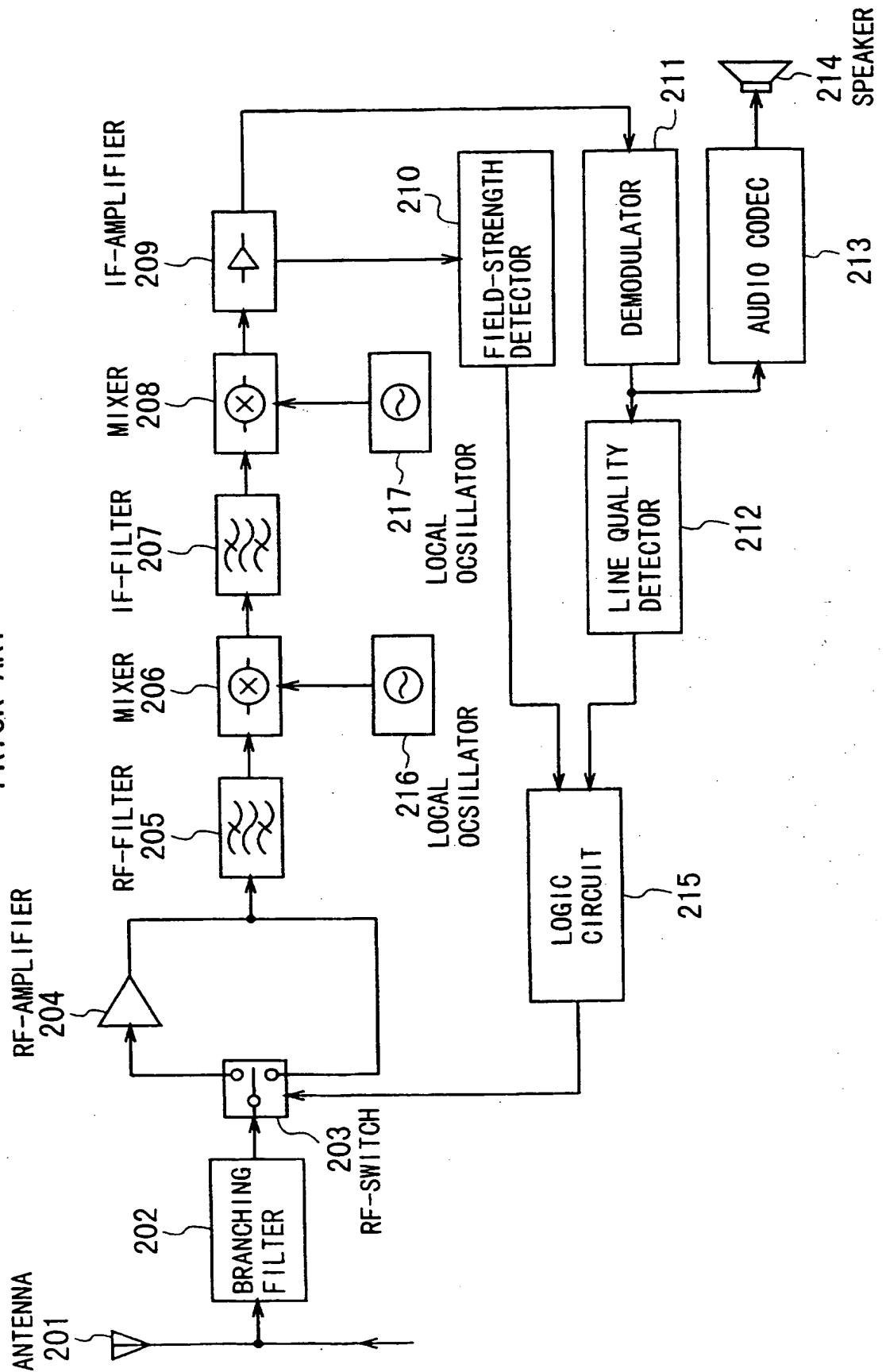


FIG. 3

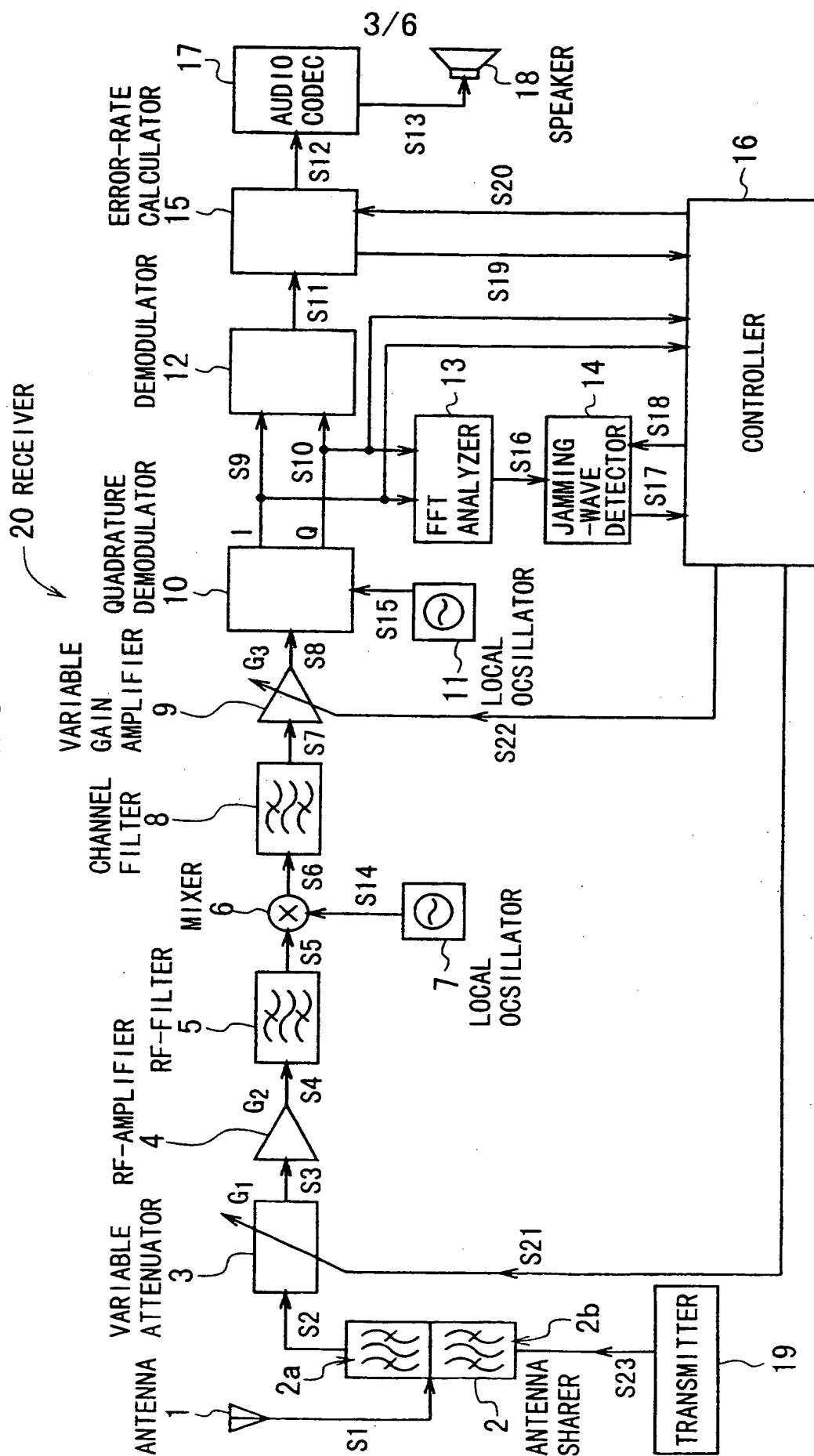


FIG. 4

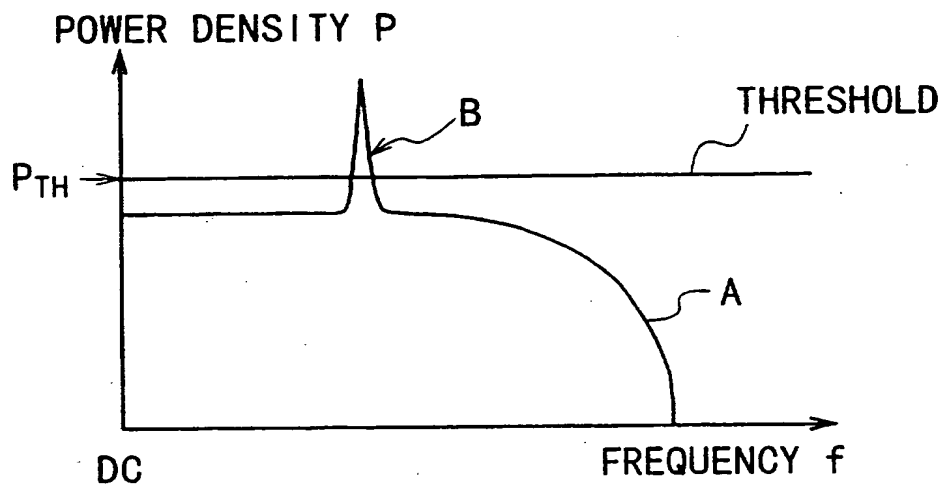


FIG. 5

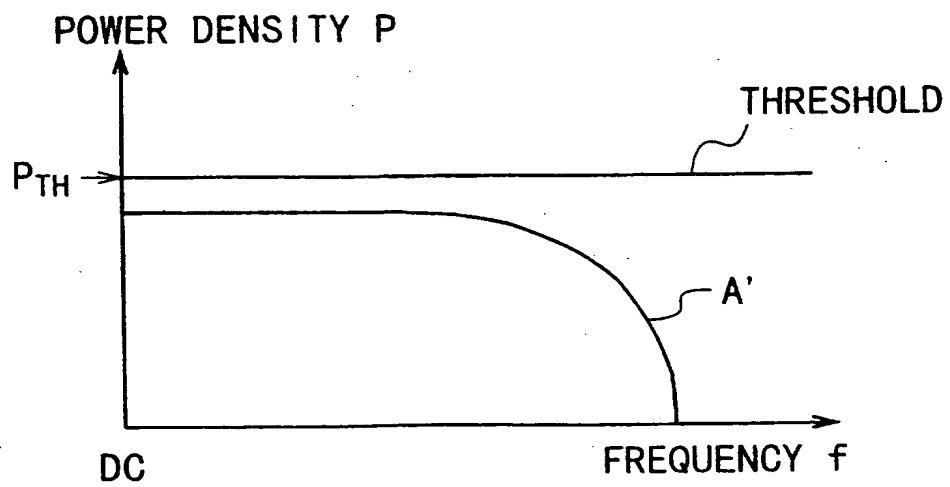


FIG. 6A

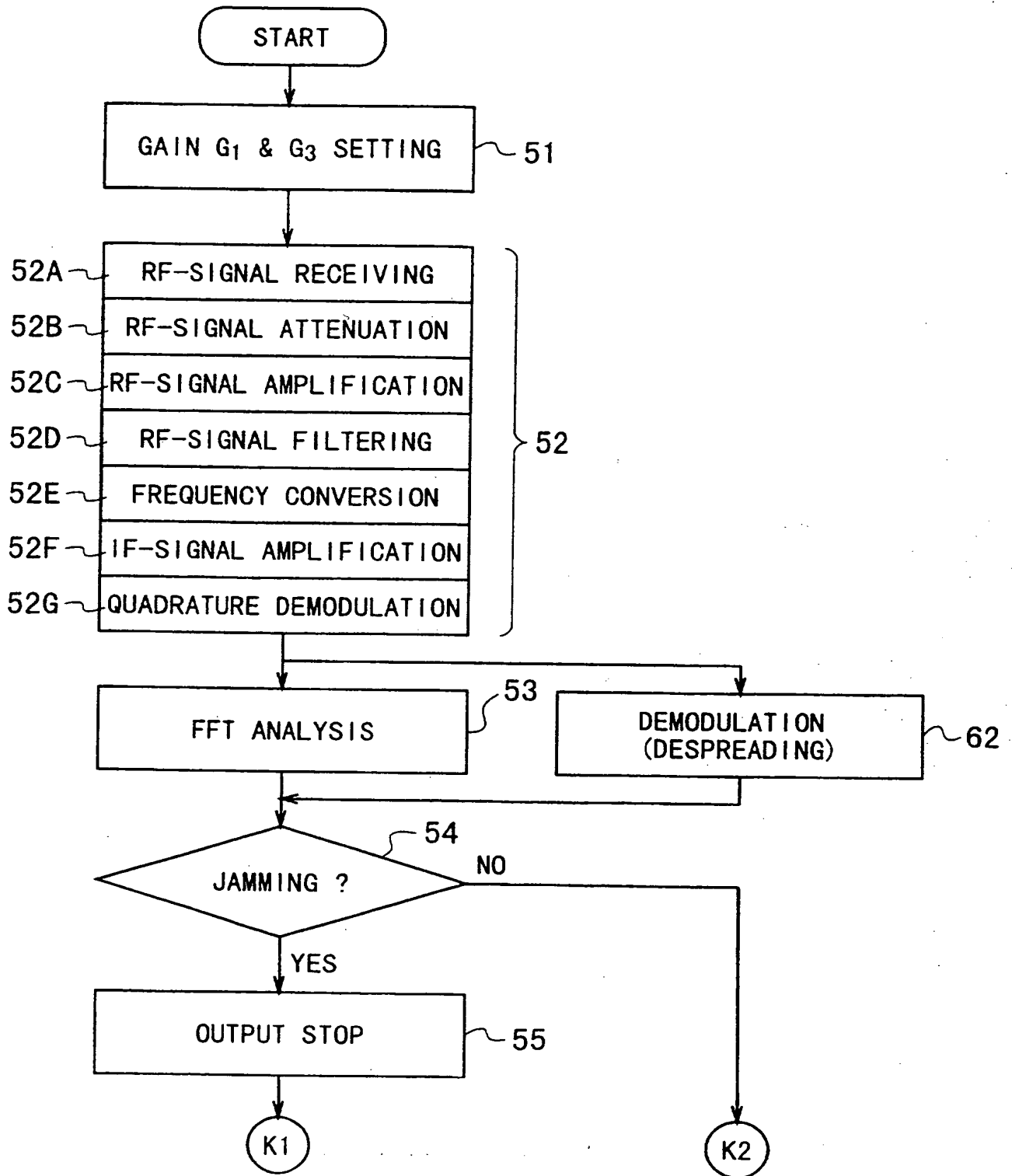
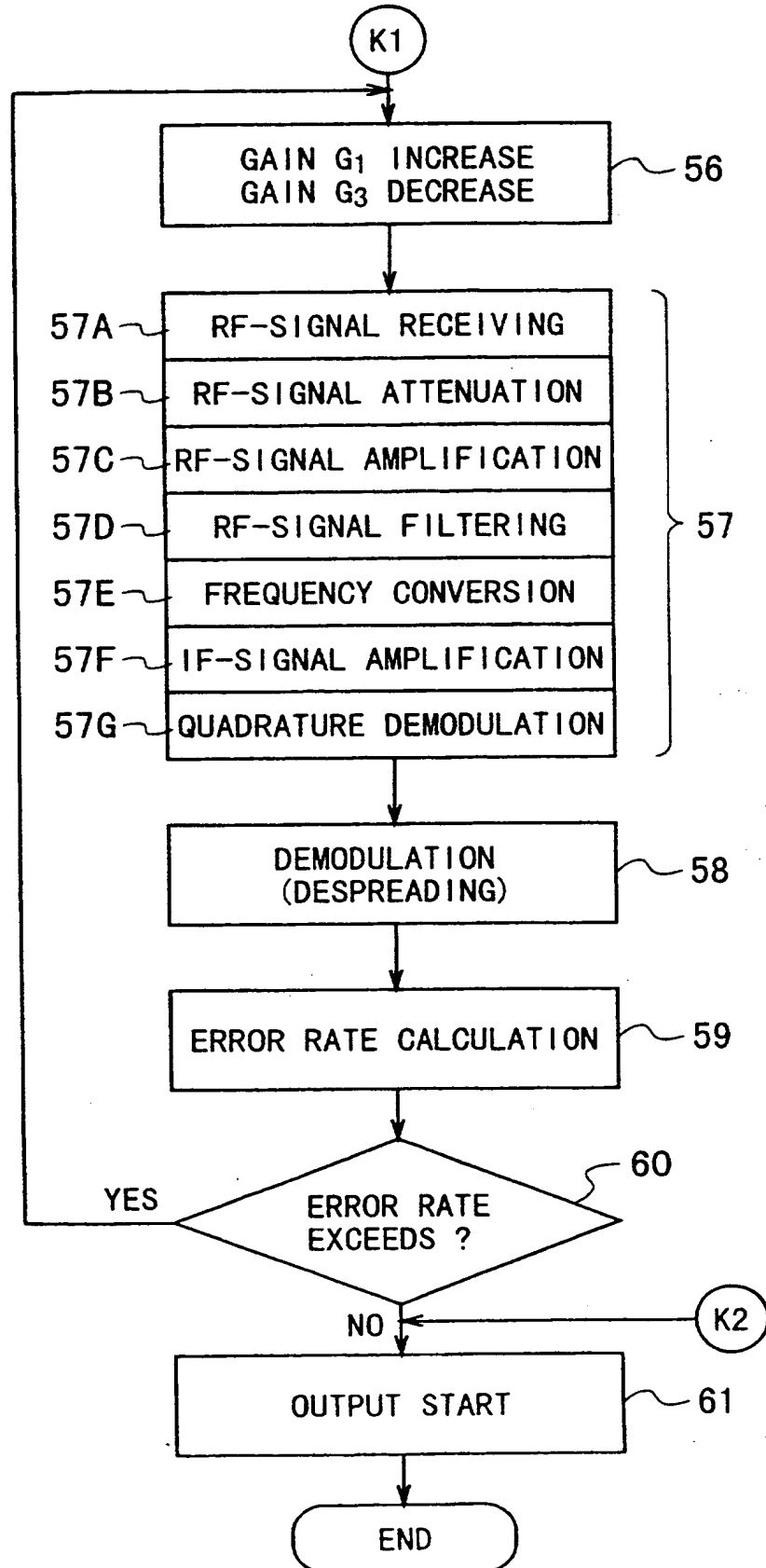


FIG. 6B



RADIO RECEIVER

5 The present invention relates to a radio receiver and more particularly, to a radio receiver for receiving a spread spectrum signal bearing an information or message the bandwidth of which has been spread out to a wider bandwidth in a transmitter.

2. Description of the Prior Art

10 When two or more jamming waves whose frequencies are outside the frequency range of a wanted signal wave are received by a radio receiver used for mobile phones or selective calling, the signal wave may be affected by the jamming waves. This is because the two or more jamming waves may be mixed together due
15 to the nonlinear characteristics of the receiver, thereby generating a mixed jamming wave whose frequency is inside the frequency range of the wanted signal wave. This problem has been known as the "inter-modulation (IM)" and considered as an important factor that affects the characteristic or performance
20 of the receiver.

 If the inter-modulation occurs, in spite of the sufficient strength or intensity of the electric field of the wanted signal wave, the bit error rate will degrade in digital communication and the SINAD will degrade in analog communication.

Here, the abbreviation "SINAD" means the ratio of Signal, Noise, and Distortion to Noise and Distortion.

To prevent the received signal strength from degrading due to the IM-induced jamming wave, various improved receivers
5 of this sort have been developed and reported.

An example of the conventional radio receivers is shown in Fig. 1, which is disclosed in the Japanese Non-Examined Patent Publication No. 5-335857 published in 1993.

In Fig. 1, a signal wave that has been transmitted from
10 a base station (not shown) is received by an antenna 101. The signal wave thus received is amplified by an Radio-Frequency (RF) amplifier 102 and then applied to a RF filter 104 through a variable attenuator 103. The bandwidth of the signal wave is limited by the RF filter 104. The signal wave whose bandwidth is limited
15 is then divided into the In-phase (I) component and the Quadrature (Q) component, and the I and Q components are applied to frequency mixers or converters 105 and 106, respectively.

The frequency mixer 106 is applied with a local signal of a specific local frequency outputted from a local oscillator
20 107. On the other hand, the local signal is applied to a phase shifter 108 and is phase-shifted by $(\pi/2)$, resulting in a phase-shifted local signal. The phase-shifted local signal is applied to the frequency mixer 105.

In the frequency mixer 105, the I component of the signal wave is frequency-mixed with the phase-shifted local signal. The frequency-mixed I component of the signal wave is then applied to a baseband filter 109, resulting in the I component in the baseband frequency range. The I component of the signal wave in the baseband frequency range is further applied to a demodulator 111.

In the frequency mixer 106, the Q component of the signal wave is frequency-mixed with the local signal. The frequency-mixed Q component of the signal wave is then applied to a baseband filter 110, resulting in the Q component in the baseband frequency range. The Q component of the signal wave in the baseband frequency range is further applied to the demodulator 111.

The I and Q components of the signal wave are digitally demodulated in the demodulator 111 to thereby recover a digital information signal transmitted by the received signal wave. The digital information signal is then applied to a controller circuit 112.

In the controller circuit 112, the frame synchronization signal included in the demodulated information signal is detected. If the frame synchronization signal is not detected within a specific period of time, the controller 112 treats the demodulated information signal as de-synchronized and informs a gain

controller 113 of the de-synchronization. To respond this information, the gain controller 113 controls the attenuation amount of the variable attenuator 103.

Thereafter, the same procedure as above is repeated until
5 the frame synchronization signal included in the demodulated information signal is detected in the controller circuit 112. When the frame synchronization signal included in the demodulated information signal is detected in the controller circuit 112, the attenuation amount of the variable attenuator 103 is returned to
10 its original value.

With the conventional radio receiver shown in Fig. 1, as described above, the attenuation amount of the variable attenuator 103 is estimated in advance. Then, if the frame synchronization signal is not detected within a specific period
15 of time in spite of the sufficient strength or intensity of the electric field of the wanted signal wave, the controller 112 treats the demodulated information signal by the demodulator 111 as de-synchronized (i.e., occurrence of the inter-modulation) and controls the attenuation amount of the variable attenuator
20 103 through the gain controller 113.

Thus, the jamming waves induced by the inter-modulation that occurs in the subsequent stages to the variable attenuator 103 are able to be suppressed.

Another example of the conventional radio receivers is shown in Fig. 2, which is disclosed in the Japanese Non-Examined Patent Publication No. 7-106993 published in 1995.

In Fig. 2, a signal wave that has been transmitted from a base station (not shown) is received by an antenna 201. The received signal is then applied to a RF switch 203 through a branching filter 202. The RF switch 203 sends the received signal to a RF filter 205 directly or through a RF amplifier 204 according to a control signal outputted from a logic circuit 215.

When the received signal is sent to the RF filter 205 through the RF amplifier 204, the received signal is amplified by the RF amplifier 204 and then, it is inputted into the RF filter 205. On the other hand, when the received signal is directly sent to the RF filter 205, the received signal is directly inputted into the RF filter 205 without amplification. The RF filter 205 removes the unnecessary waves outside the frequency range of the received signal.

A first frequency converter or mixer 206 frequency-converts the received signal using a first local signal generated by a first local oscillator 216, thereby producing a first Intermediate-Frequency (IF) signal. The frequency range of the first IF signal is limited by an IF filter 207.

A second frequency converter or mixer 208 frequency-converts the received signal thus frequency-range-limited using

a second local signal generated by a second local oscillator 217, thereby producing a second IF signal. The second IF signal is then amplified by an IF amplifier 209. The amplified second IF signal is inputted into a field-strength detector circuit 210 and
5 a demodulator circuit 211.

The demodulator circuit 211 demodulates the amplified second IF signal thus inputted and outputs a demodulated signal to a line-quality detector circuit 212 and an audio codec 213. The audio codec 213 converts the demodulated signal to an audio
10 signal and drives a speaker 214 according to the audio signal.

The field-strength detector circuit 210 outputs a high-level (H) output signal to a logic circuit 215 when the electric-field strength of the received signal wave is equal to or greater than a specific level, and a low-level (L) output signal
15 to the logic circuit 215 when the electric-field strength of the received signal wave is less than the specific level.

The line-quality detector circuit 212 detects the line quality and outputs a high-level (H) output signal to the logic circuit 215 when the line quality is greater than a specific level, and a low-level (L) output signal to the logic circuit 215 when
20 the line quality is equal to or less than the specific level.

The logic circuit 215 controls the switching operation of the RF switch 203 according to the output signals of the field-strength detector circuit 210 and the line-quality detector

circuit 212.

Specifically, when the output signal of the field-strength detector circuit 210 is in the L level, the logic circuit 215 controls the RF switch 203 so that the output of the branching filter 202 is sent to the RF amplifier 204, which is independent of the level of the output signal of the line-quality detector circuit 212.

When the output signals of the field-strength detector circuit 210 and line-quality detector circuit 212 are in the H level, the logic circuit 215 controls the RF switch 203 so that the output of the branching filter 202 is sent to the RF amplifier 204.

When the output signal of the field-strength detector circuit 210 is in the H level, and the output signal of the line-quality detector circuit 212 is in the L level, the logic circuit 215 controls the RF switch 203 so that the output of the branching filter 202 is directly sent to the RF filter 205.

With the conventional radio receiver shown in Fig. 2, as described above, when the electric-field strength of the received signal wave is large enough (i.e., equal to or greater than the specific level) and at the same time, the line quality is excessively degraded (i.e., equal to or less than the specific level), the logic circuit 215 controls the RF switch 203 so that the received signal wave outputted from the branching filter 202

is directly sent to the RF filter 205 while bypassing the RF amplifier 204.

Thus, if a plurality of jamming waves exist, the received signal wave is inputted into the first frequency converter 206 without amplification. This means that the jamming signal induced by the inter-modulation or cross-modulation in the first frequency converter 206 is able to be suppressed.

However, the above-described conventional radio receivers shown in Figs. 1 and 2 have the following problems.

10 With the conventional radio receiver shown in Fig. 1, as described above, the sensitivity degradation of the receiver is judged by detecting the frame synchronization signal. Therefore, if this radio receiver is applied to the communication system using a plurality of channels whose transmission powers are
15 different from one another, the sensitivity degradation of the receiver is not always suppressed.

For example, in the mobile communication system regulated by the Telecommunications Industry Association (TIA) as the Interim Standard 95 (IS95) in north America, three channels, i.e.,
20 the pilot channel, the traffic channel, and the synchronization channel are used. The pilot channel (PLT) is used to transmit the synchronization signal with the spreading code in the transmitter. The traffic channel (TRF) is used to transmit the information to be transmitted, which is scrambled. The

synchronization channel (SYNC) is used to transmit the synchronization signal with the frame in the transmitter and the releasing or removing signal for the scramble in the traffic channel. The transmission power of these three channels are set
5 to satisfy the relationship of $PLT : TRF : SYNC = 3 : 1 : 1$.

In this mobile communication system, the pilot channel signal is transmitted with a higher electric power than those of the traffic and synchronization channel signals. Therefore, even if the receiver is synchronized with the spreading code in
10 the transmitter through the pilot channel, the synchronization channel may be affected by the inter-modulation or cross-modulation. This means that the scramble in the traffic channel is unable to be removed.

As a result, even if the receiver is able to be
15 synchronized with the spreading code in the transmitter through the pilot channel, the sensitivity degradation (or, the electric-field strength lowering) of the receiver may take place due to the inter- or cross-modulation. This is a first problem of the conventional radio receiver shown in Fig. 1.

20 A second problem of the conventional radio receiver shown in Fig. 1 is that the demodulator 111 needs to have a very wide dynamic range. This is caused by the fact that the electric-field strength of the received signal wave is controlled or adjusted by the variable attenuator 103 and therefore, the level

of electric-field strength of the demodulated signal inputted into the demodulator 111 is not kept in a specific level.

For example, if the attenuation amount of the variable attenuator 103 is increased to suppress the effect of the jamming
5 wave, the electric-field strength of the received signal wave will be lowered compared with the case where no jamming wave exists.

A problem of the conventional radio receiver shown in Fig. 2 is that the demodulator 211 needs to have a very wide dynamic range. This is caused by the fact that the electric-field
10 strength of the received signal wave and the line quality are monitored and then, the electric-field strength of the received signal wave is controlled or adjusted by the RF switch 203 through the logic circuit 215 according to the monitoring result. Therefore, the level of electric-field strength of the
15 demodulated signal inputted into the demodulator 211 is not kept in a specific level.

For example, if the received signal wave is not amplified by the RF amplifier 204 to suppress the effect of the jamming wave, the electric-field strength of the received signal wave will be
20 lowered compared with the case where no jamming wave exists.

Accordingly, an object of at least the preferred embodiment of the present invention is to provide a radio receiver that prevents the sensitivity of the

receiver from degrading even if the receiver is applied to a communication system using a plurality of channels with unequal transmission powers.

Another such object is to provide a
5 radio receiver that prevents the sensitivity of the receiver from degrading even if a demodulator does not have a wide dynamic range.

The above objects together with others not specifically mentioned will become clear to those skilled in the art from the following description.

10 A radio receiver according to the present invention is comprised of an antenna for receiving an RF signal, a variable-gain RF amplifier for amplifying the RF signal to output an amplified, gain-controlled RF signal, a frequency converter for frequency-converting the amplified, gain-
15 controlled RF signal to output an IF signal, a variable-gain IF amplifier for amplifying the IF signal to output an amplified, gain-controlled IF signal, a despreader for despreading the amplified, gain-controlled IF signal to output a baseband signal, a demodulator for demodulating the baseband signal to output an
20 information signal, a Fourier transformer for conducting a Fourier transformation with respect to the baseband signal, a jamming-wave detector for detecting a jamming wave existing in a frequency range of the RF signal, an error-rate calculator for calculating an error rate of the information signal.

and a controller for controlling gains of the variable-gain RF amplifier and the variable-gain IF amplifier.

With the aforementioned radio receiver

the error rate of the information signal is calculated
5 by the error-rate calculator and then, the gains of the variable-gain RF amplifier and the variable-gain IF amplifier are controlled by the controller based on the calculated error rate.

Therefore the sensitivity of the receiver can be prevented
10 from degrading even if the receiver is applied to a communication system using a plurality of channels with unequal transmission powers.

Also, the gains of the variable-gain RF amplifier and the variable-gain IF amplifier may be controlled by the controller so
15 that the electric-field strength of the baseband signal is kept constant at the input of the demodulator. Thus, the sensitivity of the receiver can be prevented from degrading even if the demodulator does not have a wide dynamic range.

In a preferred embodiment of the present invention, the
20 variable-gain RF amplifier is formed by a variable-gain attenuator and a fixed-gain RF amplifier.

In another preferred embodiment of the present invention, a gain of the variable-gain RF amplifier is set as the lowest value at the start of operation.

In still another preferred embodiment of the present invention, existence and absence of a jamming wave is judged by searching a peak greater than a threshold level
5 in a spectrum obtained by Fourier transformation.

Preferred features of the present invention will now be described, by way of example only, with reference to the accompanying drawings, in which:

10

Fig. 1 is a block diagram showing a conventional radio receiver.

Fig. 2 is a block diagram showing another conventional
15 radio receiver.

Fig. 3 is a block diagram showing an embodiment of a radio receiver.

20 Fig. 4 is a graph showing the electric power spectrum obtained by FFT in the radio receiver according to the embodiment of Fig. 3, where an inter-modulated-induced jamming wave exists.

25 Fig. 5 is a graph showing the electric power spectrum obtained by FFT in the radio receiver according to the embodiment of Fig. 3, where no inter-modulation-induced jamming wave exists.

30

35

Fig. 6A is a flowchart showing the process steps of the receiving method in the radio receiver according to the embodiment of Fig. 3.

Fig. 6B is a flowchart showing the process steps of the receiving method in the radio receiver according to the embodiment of Fig. 3.

10

Fig. 3 shows the configuration of an embodiment of a radio receiver 20.

In this receiver 20, as shown in Fig. 3, a common antenna 15 1 receives various waves that have been transmitted from a base station (not shown). Information signals S1 transmitted by the waves thus received are sent to an antenna sharer 2 having a receiving filter 2a and a transmitting filter 2b and then, unnecessary ones of the received waves are removed by the receiving filter 2a. Thus, a wanted information signal S2 is selected and sent to a variable attenuator 3 having a variable gain G1.

An information signal S23 to be transmitted by a transmitter 19 is sent to the transmitting filter 2b and then,

it is transmitted from the antenna 1 while unnecessary signals are removed by the transmitting filter 2b.

The variable attenuator 3, which serves as a first gain controller for controlling the gain of the signal S2, outputs an attenuated signal S3 to a RF amplifier 4 with a fixed gain G2.

The RF amplifier 4 amplifies the attenuated signal S3 at the gain G2 and outputs an amplified signal S4 to a RF filter 5.

The RF filter 5 allows the RF components of the amplified signal S4 to pass through. The signal filtered by this filter is defined as S5.

A frequency mixer or converter 6 converts the filtered signal S5 to an IF signal S6 of an IF range using a local signal S14 of a first local frequency that is outputted from a first local oscillator 7. The IF signal S6 is then sent to a channel filter 8.

The channel filter 8 allows the IF signal S6 to pass through. The IF signal filtered by this filter 8 is defined as S7.

A variable-gain amplifier 9 with a variable gain G3 amplifies the IF signal S7 and outputs an IF amplified signal S8. The variable-gain amplifier 9 serves as a second gain controller for controlling the gain of the signal S7.

A quadrature demodulator 10 frequency-mixes the amplified IF signal S8 with a second local signal with a second

local frequency that is outputted from a second local oscillator 11, thereby quadrature-demodulating the amplified IF signal S8. Thus, the I and Q components S9 and S10 of the baseband frequency are outputted to a demodulator 12, a FFT analyzer 13, and a
5 controller circuit 16.

The demodulator 12 demodulates the baseband I and Q components S9 and S10 and outputs an information signal S11 to an error-rate calculator 15.

The error-rate calculator 15 calculates the bit error
10 rate of the demodulated baseband signal S11 and outputs a signal S12 to an audio codec 17 and a signal S20 to the controller circuit 16.

The audio codec 17 outputs a signal S13 to drive a speaker 18 according to the signal S12 from the error-rate calculator 15.

15 The FFT analyzer 13 conducts a Fast Fourier transformation (FFT) using the baseband I and Q components S9 and S10 and outputs a signal S16 to a jamming-wave detector 14.

The detector 14 searches the signal S16 and judges whether a jamming wave exists in the received signal S2 due to inter- or
20 cross-modulation or not. The detector 14 outputs a result signal S17 to the controller circuit 16.

The controller circuit 16 calculates the electric power of the received signal S2 based on the I and Q components S9 and S10 sent from the quadrature demodulator 10. The controller

circuit 16 outputs control signals S18 and S20 to the jamming-wave detector 14 and the error-rate calculator 15, respectively. Further, the controller circuit 16 outputs control signals S21 and S22 to the variable attenuator 3 and the variable-gain amplifier 9, respectively.

Next, the operation of the radio receiver 20 according to the embodiment of Fig. 3 is explained below with reference to Figs. 6A and 6B.

In the step 51, the gain G1 of the variable attenuator 3 is set as the lowest value by the controller circuit 16 through the control signal S21. At the same time, the gain G3 of the variable-gain amplifier 9 is set as a predetermined initial value by the controller circuit 16 through the control signal S22.

Here, the gains G1 and G3 are adjusted proportional to the voltage values of the control signals S21 and S22, respectively.

In the step 52, a sequence from signal receiving to quadrature demodulation is performed.

Specifically, in the substep 52A, the transmitted signal waves S1 are received by the antenna 1 and the wanted RF signal S2 is selected by the antenna sharer 2.

In the substep 52B, the wanted RF signal S2 is attenuated by the attenuator 3 according to the adjusted gain G1.

In the substep 52C, the attenuated RF signal S3 is amplified by the RF amplifier 4 according to the fixed gain G2.

In the substep 52D, the amplified RF signal S4 is filtered by the RF filter 5 so that the components of the RF frequencies
5 of the signal S4 are passed through the filter 5.

In the substep 52E, the filtered RF signal S5 is frequency-converted by the mixer 6 by mixing the frequency of the RF signal S5 with the local frequency of the first local signal S14, thereby producing the IF signal S6. The IF signal S6 thus
10 produced is then filtered by the channel filter 8 to select the wanted channel, resulting in the filtered IF signal S7.

In the substep 52F, the filtered IF signal S7 is amplified by the variable-gain amplifier 9 according to the adjusted gain G3. The gain G3 is controlled so that the electric-field strength
15 of the amplified IF signal S8 is kept constant independent of the electric-field strength of the input IF signal S7.

In the substep 52G, the amplified signal S8 whose electric-field strength is kept constant is subjected to the quadrature demodulation by the demodulator 10 using the second
20 local signal S15 from the second local oscillator 11. Thus, the Q and I component signals S9 and S10 of the baseband frequencies are produced.

In the step 53, a Fast Fourier transformation (FFT) analysis for the Q and I component signals S9 and S10 of the

baseband frequencies is conducted by the FFT analyzer 13. The result of this FFT analysis is sent to the jamming-wave detector 14 as the signal S16.

Figs. 4 and 5 schematically show the power density spectra obtained by the FFT analysis in the step S53.

When some jamming wave exists, as shown in Fig. 4, the curve A of the power density has a peak B greater than the threshold level P_{TH} of the power density P at a specific frequency f. On the other hand, when no jamming wave exists, as shown in Fig. 5, the curve A' of the power density has no peak. Therefore, the existence of the jamming wave is able to be found by searching existence and absence of the peak B in the power density spectrum.

The frequency range to be observed in the FFT analysis is the same as the expanded bandwidth given by spreading out. The sampling or integrating frequency width is the same as the non-spread, narrow bandwidth.

Returning to Figs. 6A and 6B, in the step 62, the Q and I component signals S9 and S10 of the baseband frequencies are demodulated by the demodulator 12, thereby outputting the signal S11 to the error-rate calculator 15.

In the step 54, based on the result of the FFT analysis in the step 53, it is judged whether some jamming signal or wave exists or not.

When the answer is "No" in the step 60, which means that no jamming wave exists, the jamming-wave detector 14 sends the signal S17 to the controller circuit 16, thereby informing the controller circuit 16 of the absence of the jamming wave. Then,
5 the controller 16 sends the control signal S20 to the error-rate calculator 15, thereby starting the output of the error-rate calculator 15 to the audio codec 17 in the step 61.

When the answer is "Yes" in the step 60, which means that some jamming wave exists, the jamming-wave detector 14 sends the
10 signal S17 to the controller circuit 16, thereby informing the controller circuit 16 of the existence of the jamming wave. Then, the controller 16 sends the control signal S20 to the error-rate calculator 15, thereby stopping the output of the error-rate calculator 15 to the audio codec 17 in the step 55.

15 Simultaneously, in this case, the controller circuit 16 adjusts the gains G1 and G3 of the variable attenuator 3 and the variable-gain amplifier 9 by a predetermined value with the use of the control signals S21 and S22. That is, as shown in the step 56, the gain G1 is increased by a predetermined increment and/or
20 the gain G3 is decreased by a predetermined decrement. Subsequently, the step 57 including the substeps 57A to 57G is performed with respect to the next frame of the transmitted signals S1, where the substeps 57A to 57G are substantially the same as the substeps 52A to 52G in the step 52, respectively.

In the step 58, like the step 62, the Q and I component signals S9 and S10 of the baseband frequencies are demodulated by the demodulator 12, thereby outputting the signal S11 to the error-rate calculator 15.

5 In the step 59, the error-rate calculator 15 calculates the bit error rate in the baseband signal S11. The error-rate data thus calculated is sent to the controller circuit 16 as the signal S19.

In the step 60, the controller circuit 16 judges whether
10 the value of the calculated error rate exceeds a predetermined reference value or not.

When the answer is "Yes" in the step 60, which means that the value of the calculated error rate exceeds the reference value, the flow is returned to the step 56. Then, the controller circuit
15 16 changes the gain G1 and/or G2 again in the step 56 and the steps 57 to 60 are repeated until the answer is "No".

When the answer is "No" in the step 60, which means that the value of the calculated error rate does not exceed the reference value, the flow is progressed to the step 61. Then,
20 the controller circuit 16 sends the signal S29 to the error-rate calculator 15, thereby informing the controller circuit 16 of the non-excess of the error rate.

Then, the controller 16 sends the control signal S20 to the error-rate calculator 15, thereby starting the output of the

order distortion generated by the non-uniform operation or characteristic of the RF amplifier 4 and the RF frequency converter 5, where n is a constant greater than unity.

The n -th order distortion increases (or decreases) by $n\alpha$ decibels (dB) if the electric-field strength of the received signal wave is increased by an increment (or decrement) of α dB, where α is a positive constant. Considering this property or characteristic of the n -th order distortion, the variable attenuator 3 serving as a part of the variable-gain RF amplifier is located at the front end and the variable-gain amplifier 9 is located to amplify the IF signal S7. Moreover, the gains G1 and G3 of the variable-gain attenuator 3 and the variable-gain IF amplifier 9 are controlled by the controller circuit 16 so that the electric-field strength of the baseband signals S9 and S10 are kept constant at the input of the demodulator 12.

It is clear that the radio receiver 20 according to the embodiment described above is designed for receiving an information-bearing signal wave whose bandwidth has been spread or expanded in a transmitter using the well-known spread spectrum technique.

However, the receiver 20 is effective if it is applied to the case where a first communication system using the spread or expanded bandwidth and a second communication system using the

narrow (i.e., non-spread or non-expanded) bandwidth are simultaneously utilized in a common frequency range. This is because the signal waves used in the second communication system will become a jamming wave in the first communication system. An
5 example of this case is the north America where the mobile communication system regulated by the TIA as the IS95 is applied.

While the preferred form of the present invention has been described, it is to be understood that modifications will be apparent to those skilled in the art without departing from the
10 scope of the invention. The scope of the invention, therefore, is to be determined solely by the following claims.

Each feature disclosed in this specification (which term includes the claims) and/or shown in the drawings may be incorporated in the invention independently of other disclosed and/or illustrated features.

The text of the abstract filed herewith is repeated below as part of the specification.

A radio receiver is provided, which prevents the sensitivity of the receiver from degrading even if the receiver is applied to a communication system using a plurality of channels with unequal transmission powers. This radio receiver is comprised of an antenna for receiving a wanted RF signal, a variable-gain RF amplifier for amplifying the wanted RF signal to output an amplified, gain-controlled RF signal, a frequency converter for frequency-converting the amplified, gain-controlled RF signal to output an IF signal, a variable-gain IF amplifier for amplifying the IF signal to output an amplified, gain-controlled IF signal, a despreader for despreding the amplified, gain-controlled IF signal to output a baseband signal, a Fourier transformer for conducting a Fourier transformation with respect to the baseband signal, a jamming-wave detector for detecting a jamming wave existing in a frequency range of the wanted RF signal, a demodulator for demodulating the baseband signal to output an information signal, an error-rate calculator for calculating an error rate of the information signal, and a controller for controlling gains of the variable-gain RF amplifier and the variable-gain IF amplifier.

The radio receiver thus uses a spread-spectrum technique.

CLAIMS

1. A radio receiver comprising:

- 5 an antenna for receiving an RF signal;
 a variable-gain RF amplifier for amplifying said
RF signal to output an amplified, gain-controlled RF signal;
 a frequency converter for frequency-converting said
amplified, gain-controlled RF signal to output an IF signal;
10 a variable-gain IF amplifier for amplifying said IF
signal to output an amplified, gain-controlled IF signal;
 a despreader for despreading said amplified, gain-
controlled IF signal to output a baseband signal;
 a Fourier transformer for conducting a Fourier
15 transformation with respect to said baseband signal;
 a jamming-wave detector for detecting a jamming wave
existing in a frequency range of said RF signal;
 a demodulator for demodulating said baseband signal to
output an information signal;
20 an error-rate calculator for calculating an error rate
of said information signal; and
 a controller for controlling gains of said variable-gain
RF amplifier and said variable-gain IF amplifier.

2. A radio receiver as claimed in Claim 1, wherein the gains of said variable-gain IF amplifier are controllable so that the electric-field strength of said baseband signal is kept constant at an input of said demodulator.

3. A receiver as claimed in Claim 1 or 2, wherein said variable-gain RF amplifier is formed by a variable-gain attenuator and a fixed-gain RF amplifier.

10

4. A receiver as claimed in any preceding claim, wherein a gain of said variable-gain RF amplifier is to be set to the lowest value at the start of operation.

15 5. A receiver as claimed in any preceding claim, adapted to determine existence or absence of a jamming wave by searching a peak greater than a threshold level in a spectrum obtained by Fourier transformation.

20 6. A radio receiver substantially as herein described with reference to Figure 3 of the accompanying drawings.

25

30

35



Application No: GB 9813556.9
Claims searched: 1-6

Examiner: D. Midgley
Date of search: 24 August 1998

Patents Act 1977
Search Report under Section 17

Databases searched:

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:
UK Cl (Ed.P): H3G GPP, GPDP, GPXX, GSX H3W WUL, WVT, WVX
Int Cl (Ed.6): H03G H04B 1/69, 1/707 H04J 13/00, 13/02, 13/04
Other: ONLINE:WPI

Documents considered to be relevant:

Category	Identity of document and relevant passage	Relevant to claims
A	WO 94/03002 A1 (COMSOURCE)	1

28

X	Document indicating lack of novelty or inventive step	A	Document indicating technological background and/or state of the art.
Y	Document indicating lack of inventive step if combined with one or more other documents of same category.	P	Document published on or after the declared priority date but before the filing date of this invention.
&	Member of the same patent family	E	Patent document published on or after, but with priority date earlier than, the filing date of this application.

THIS PAGE BLANK (USPTO)